

<b>Notice of References Cited</b>	Application/Control No. 09/823,235	Applicant(s)/Patent Under Reexamination WANG ET AL.	
	Examiner Shane F Gerstl	Art Unit 2183	Page 1 of 2

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,073,213	06-2000	Peled et al.	711/125
*	B	US-5,357,617	10-1994	Davis et al.	712/245
*	C	US-4,916,652	04-1990	Schwarz et al.	708/510
*	D	US-6,092,187	07-2000	Killian, Earl A.	712/239
*	E	US-6,044,222	03-2000	Simons et al.	717/156
*	F	US-6,594,824	07-2003	Volkonsky et al.	717/159
*	G	US-5,937,195	08-1999	Ju et al.	717/156
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Ferreira de Souza, A and Rounce, P; Dynamically Scheduling the Trace Produced During Program Execution into VLIW Instructions; Parallel and Distributed Processing, 1999; Pages 248-257
*	V	Jacobsen E, Rotenberg E, and Smith J E; Assigning Confidence to Conditional Branch Predictions; Microarchitecture, 1996; pages 142-152
	W	Nair, Ravi and Hopkins, Martin E.; Exploiting Instruction Level Parallelism in Processors by Caching Scheduled Groups; 1997; ISCA '97 Denver, CO, USA; Pages 13-25
	X	The Authoritative Dictionary of IEEE Standards Terms; 2000; IEEE Press; 7th Edition; Pages 52 and 123-124

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

<b>Notice of References Cited</b>	Application/Control No. 09/823,235	Applicant(s)/Patent Under Reexamination WANG ET AL.	
	Examiner Shane F Gerstl	Art Unit 2183	Page 2 of 2

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Tullen, Dean M., eggars, Susan J., Levy, Henry M.; Simultaneous multithreading: Maximizing On-Chip Parallelism; 1995; ISCA '95 Santa Margherita Ligure Italy; Pages 392-403
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.